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020457 7590 04/15/2004

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/684,414	10/15/2003	Mitsuru Hiraki	501.37777CC8	4395

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, MEMORY MODULE, STORAGE DEVICE AND THE METHOD FOR REPAIRING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	07/15/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, HIEN N	2824	365-194000

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Antonelli, Terry, Stout & Kraus, LLP

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hitachi, Ltd.

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) (Date) **July 15, 2004**
Gregory E. Montone # 28,141

07/16/2004 BSAYAS12 00000017 10684414

01 FC:1501 1330.00 OP
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